



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: **Jin-Sheng Wang, et al.**

Serial No.: **10/649,221**

Filed: **08/25/2003**

For: **Temperature Independent CMOS Reference
Voltage Circuit for Low-Voltage Applications**

Docket No.: **TI-35376**

Examiner: **Zweizig, Jeffery S.**

Art Unit: **2816**

Not. of All.: **02/18/2005**

Conf. No.: **5411**

LETTER OF TRANSMITTAL

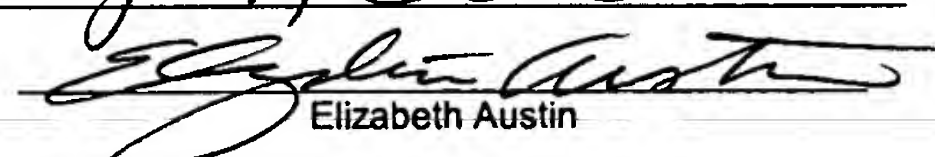
Commissioner for Patents

Mail Stop: Issue Fee

Alexandria, VA 22313-1450

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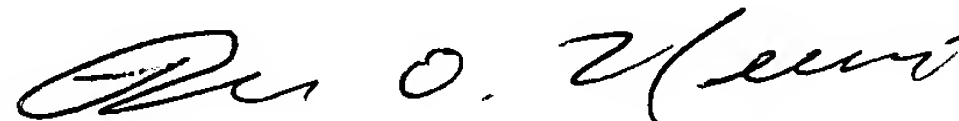
May 9, 2005

Elizabeth Austin

Sir:

Enclosed is the Issue Fee Transmittal form in the above-identified application.

Please charge the \$300 Publication Fee and the \$1,400 Issue Fee to the deposit account of Texas Instruments Incorporated, Account No. 20-0668. An original and two copies of this sheet are enclosed.

Respectfully submitted,



Ronald O. Neerings

Attorney for Applicants

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The Supplemental Declarations in the above identified application are enclosed.

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